**Active Metering of Digital Devices:**

**An Overview.**

 **A. A. Ivaniuk** 1

**S. S. Zalivaka**

1 Белорусский государственный университет информатики и радиоэлектроники

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**Abstract:** The paper presents existing active hardware metering approaches. The motivation of using active metering approaches by fabless integrated circuits design companies is given. The finite state machine based and asymmetrical cryptography based methods are presented. The advantages and disadvantages of existing active metering approaches are analyzed. The potential solution for modern technique issues are proposed.

**Keywords:** Integrated Circuits, identification, authentication, active metering, passive metering, physically unconable functions.

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