**Circuit implementation of parallel logical control algorithms represented in PRALU description**

**P. N. Bibilo1**

**Yu. V. Pottosin1**

**V. I. Romanov1**

**A. D. Zakrevskij1**

1 Belarusian State University of Informatics and Radioelectronics

**Abstract.** Software system for circuit implementation of parallel logical control algorithms LOCON-2 is described in this chapter. The system allows obtaining proper descriptions (models) in VHDL at any stage of transforming descriptions of the control algorithms. Obtaining of VHDL models provides possibility to integrate LOCON-2 with the synthesizers of logic circuits.

**Keywords:** Digital circuits implementation Parallel logical control algorithms VHDL PRALU.

[http://link.springer.com/chapter/10.1007%2F978-3-319-26725-8\_3](http://link.springer.com/chapter/10.1007/978-3-319-26725-8_3)